IN THE CLAIMS:

1. (Currently Amended) A multilayer board on whose outside layer, one or more circuit components are mounted, the multilayer board comprising:

a signal line requiring tamper-resistance, the signal line being connected to a predetermined component among the one or more circuit components and including: (a) a conductive trace and (b) a conductive via that passes through layers of the multilayer board, wherein

the conductive trace and an end of the conductive via existing on ~~an~~ the outside layer of the multilayer board are placed ~~under one or more circuit components mounted on the outside layer~~ only under the predetermined component, and not on the other area of the outside layer.

2. (Original) The multilayer board of Claim 1, wherein

the signal line further includes a conductive trace on an inner layer that is sandwiched between sheets of foil and/or circuit components placed on layers above and below the inner layer so that the sheets of foil and/or circuit components hide the conductive trace on the inner layer when viewed from above or below.

3. (Original) The multilayer board of Claim 2, wherein

the sheets of foil placed on the layers that are outside the inner layer are connected to either a ground or a power source.

4. (Original) The multilayer board of Claim 3, wherein

the conductive trace on the outside layer is further covered by a circuit component on another outside layer when viewed from above or below.

5. (Original) The multilayer board of Claim 2, wherein

the signal line requiring tamper-resistance is either a signal line that is input to an encryption unit or a signal line that is output from a decryption unit.

6. (Currently Amended) A multilayer board on whose outside layer, one or more circuit components are mounted, the multilayer board comprising:

a certain signal line that is connected to a predetermined component among the one or more circuit components and includes (a) a conductive trace and (b) a conductive via that passes through layers of the multilayer board, wherein

the conductive trace and an end of the conductive via existing on ~~an~~ the outside layer of the multilayer board are placed ~~under one or more circuit components mounted on~~ only under the predetermined component, and not on the other area of the outside layer,

the certain signal line further includes a conductive trace on an inner layer ~~that is~~ of the multilayer board, the conductive trace being sandwiched between sheets of foil and/ or circuit components placed on layers above and below the inner layer so that the sheets of foil and/or ~~circuit components~~ the predetermined component hide the conductive trace on the inner layer when viewed from above or below, and

the certain signal line is either a data line or an address line.

7-29. (Cancelled)

30. (Currently Amended) A tamper-resistant multilayer board for transfer of pixel data to be encrypted comprising:

a board member having a plurality of layers and one or more components mounted thereon;

a reception/decryption unit mounted on the board member;

an output interface unit mounted on the board member and operatively connected to the reception/decryption unit; and

a conductive path operatively designed for interconnecting the reception/ decryption unit and the output interface unit and ~~position~~ positioned adjacent an interior layer surface for a portion of the conductive path and positioned under ~~one or more components~~ the reception/decryption unit and/or the output interface unit only for the remainder of the conductive path to prevent direct access from the exterior of the board member.